

Specification

Part No.: DLC70F

DLC70F (.600" x .400")

◆ Product Features

High Q value, high power, low ESR/ESL, low noise,
high self-resonant frequency, high reliability.

◆ Product Application

Typical applications: semiconductor RF power, MRI, repeater, high speed railway signal responsor, wireless broadcasting and TV device.



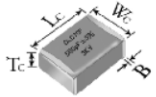
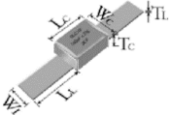
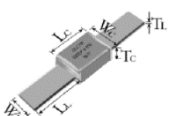
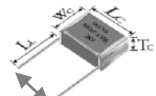
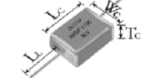
◆ DLC70F Capacitance Table

Cap.	Code	Tol.	Rated	Cap.	Code	Tol.	Rated	Cap.	Code	Tol.	Rated
pF			WVDC	pF			WVDC	pF			WVDC
1.0	1R0	A,B, C,D	5000V Code 502 or 8000V Code 802	33	330	F,G,J	5000V Code 502 or 8000V Code 802	820	821	F,G,J	2000V Code 202 or 3000V Code 302
1.2	1R2			39	390			1000	102		
1.5	1R5			47	470			1200	122		
2.2	2R2			56	560			1500	152		
2.7	2R7			68	680			1800	182		
3.3	3R3			82	820			2200	222		
3.9	3R9			100	101		2700	272	1000V Code 102 or 2000V Code 202		
4.7	4R7			120	121		3300	332			
5.6	5R6			150	151		4700	472			
6.8	6R8			180	181		5100	512			
8.2	8R2	220		221	5100		512				
10	100	270		271	5600		562				
12	120	330		331	6800		682				
15	150	390		391							
18	180	470		471							
22	220	560		561							
27	270	680		681							

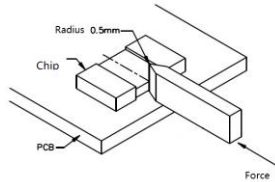
Remark: special capacitance, tolerance and WVDC are available, consult with DALICAP.

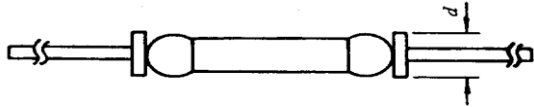
◆ **DLC70F Termination Type and Dimensions**

unit: inch (millimeter)

Series	Term.	Type/	Capacitor Dimensions				Lead Dimensions			Termina Material
	Code	Outlines	Length Lc	Width Wc	Thickness Tc	Overlap B	Length LL	Width WL	Thickness TL	
70F	P	 Chip(Non-Mag)				.024~.059 (0.60~1.50)	—	—	—	100%Sn over Copper Plating , RoHS Compliant RoHS Compliant
70F	MN	 Microstrip(Non-Mag)								
70F	AN	 Axial Ribbon (Non-Mag)	.614 +.015~ .010 (15.60 +0.38~ 0.25)	.433±.0 10 (11.00± 0.25)	.197 (5.00) max		728 (18.50) min	.350 ±.010 (8.89 ±0.25)	.008 ±.001 (0.20 ±0.025)	Silver- plated Copper
70F	RN	 Radial Wire(Non-Mag)					.748 (19.00) min	0.672±0.020 (17.06±0.50) 0315±.004 (0.80±0.10)		Silver wire
70F	BN	 Axial Wire (Non-Mag)					.906 (23.00) min	.031±.004 (0.80 ± 0.10)		

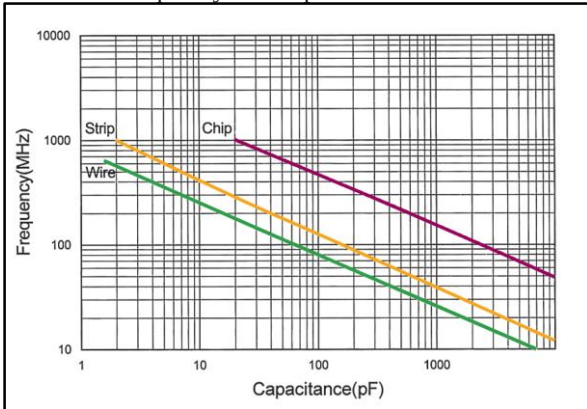
◆ **Reliability Test Conditions.**

No.	Item	Specification	Test method												
1	Operating temperature	-55℃~125℃ -55℃~175℃	-												
2	Appearance	No defects or abnormality	Visual inspection: ×10 microscope.												
3	Dimensions	See the previous page	Caliper inspection												
4	Capacitance	Shall be within the applicable tolerance specified.	Test frequency: C≤1000pF: 1MHz±10% C>1000pF: 1KHz±10%												
5	D.F	DF≤0.05%	Test voltage: 1.0±0.2Vrms												
6	Insulation resistance	10 ⁵ Megohms min. @ +25 ℃ 10 ⁴ Megohms min. @ +125 ℃	Voltage: DC Rated Voltage (500V max)												
7	Dielectric withstanding voltage (DWV)	Shall be no evidence of breakdown or visible evidence of arcing or damage.	Test Voltage: Rated voltage≤500V: 250% of the rated voltage 500V<Rated voltage≤1250V: 150% of the rated voltage Rated voltage>1250V: 120% of the rated voltage Applied Time: 5 s Charge/discharge current: 50mA max.												
8	Temperature coefficient	(0±30)ppm/℃(-55℃~125℃) (0±60)ppm/℃(+125℃~175℃)	The capacitance change should be measured after 5 min. at each specified temp. stage. Capacitance value as a reference is the value in step 3. <table><tr><th>step</th><th>temperature (℃)</th></tr><tr><td>1</td><td>25±2</td></tr><tr><td>2</td><td>-55±3</td></tr><tr><td>3</td><td>25±2</td></tr><tr><td>4</td><td>Tmax±3</td></tr><tr><td>5</td><td>25±2</td></tr></table> $TC = \frac{C_x - C_3}{C_3 \times \Delta T} \times 10^6 \text{ ppm/}^\circ\text{C}$	step	temperature (℃)	1	25±2	2	-55±3	3	25±2	4	Tmax±3	5	25±2
step	temperature (℃)														
1	25±2														
2	-55±3														
3	25±2														
4	Tmax±3														
5	25±2														
9	Capacitance Drift	±0. 2% or ±0.05pF, whichever is larger.													
10	Adhesive Strength of Termination	No removal of the terminations or other defect should occur.	Pressurizing force: 50.0^{+1.0}₀N Test time: 10±1 sec. 												

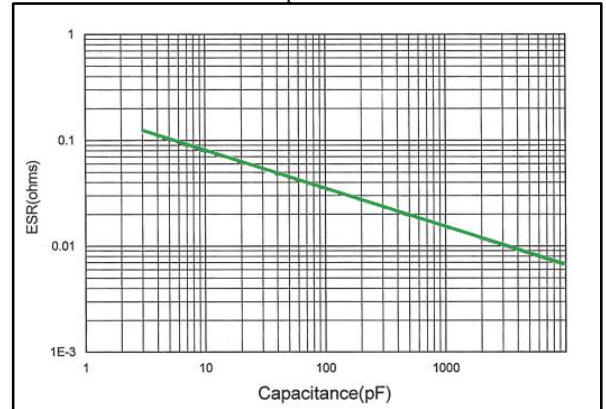
No.	Item	Specification	Test method															
11	Terminal strength	Visual examination for loosening, rupturing, and permanent damage to the terminals.	Solder the capacitor on test jig as below . Then apply a horizontal force. Holding time: 5~10 s Force: 10 N minimum. d= (1.02±0.15) mm; 															
12	Solderability of termination	Shall be at least 95 percent covered with a smooth solder coating.	Immerse the capacitor in a eutectic solution requirement temperature 245±2 °C for 2±0.5 seconds. Capacitor shall be immersed to a depth of 10mm.															
13	Resistance to soldering Heat	Appearance: No evidence of mechanical damage or delamination or exposed. Cap change: within -1.0%~+2.0% or ±0.5pF whichever is larger. Q: No less than initial requirement. IR(25°C): No less than initial requirement.	Preheat for 1 minute at 150°C to 180°C before immerse the capacitor in a eutectic solution at 260±5°C for 10±1 seconds. Capacitor shall be immersed to a depth of 10mm. And following a 24±2 hours cooling period.															
14	Temperature Cycle	Appearance: No evidence of mechanical damage . Cap change: within ±0.5% or ±0.5pF whichever is larger. Q: No less than initial requirement. IR(25°C): No less than 30% initial requirement. DWV: To meet initial requirement.	Perform the 5 cycles according to the four heat treatments listed in the following table. Set it for 24±2 hours at room temperature. <table><tr><th>step</th><th>Temperature(°C)</th><th>Time(min)</th></tr><tr><td>1</td><td>-55(-3~0)</td><td>≥30</td></tr><tr><td>2</td><td>25+10</td><td>≤5</td></tr><tr><td>3</td><td>125(0~+3)</td><td>≥30</td></tr><tr><td>4</td><td>25+10</td><td>≤5</td></tr></table>	step	Temperature(°C)	Time(min)	1	-55(-3~0)	≥30	2	25+10	≤5	3	125(0~+3)	≥30	4	25+10	≤5
step	Temperature(°C)	Time(min)																
1	-55(-3~0)	≥30																
2	25+10	≤5																
3	125(0~+3)	≥30																
4	25+10	≤5																
15	Humidity, steady state	Appearance: No evidence of mechanical damage . Cap change: within ±0.3% or ±0.3pF whichever is larger. IR(25°C): No less than initial requirement.	With 1.5 Volts D.C. applied while subjected to an environment of 85°C with 85% relative humidity for 240 hours minimum. Removed and sit 3.5±0.5 hours at room temperature.															
16	High Temperature Load (Life)	Appearance: No evidence of mechanical damage . Cap change: within ±2% or ±0.5pF whichever is larger. Q: No less than 50% initial requirement. IR(25°C): No less than 30% initial requirement.	Test Voltage: Rated voltage≤500V: 200% of the rated voltage 500V<Rated voltage≤1250V: 120% of the rated voltage Rated voltage>1250V: 100% of the rated voltage The charge/discharge current is less than 50mA. Temperature: Tmax°C; Time: 2000h. Measurement: Set it for 48 hours at room temperature, then measure.															

◆ **DLC70F Performance Curve**

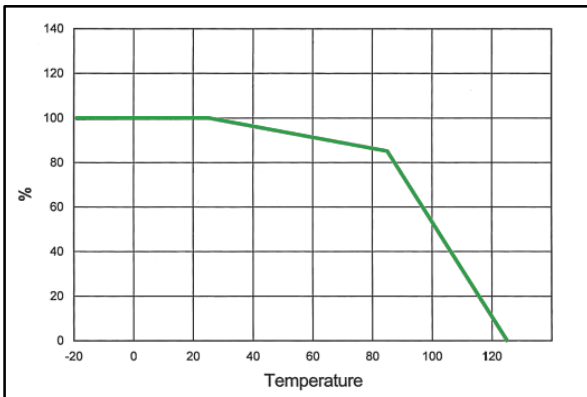
Frequency VS Capacitance



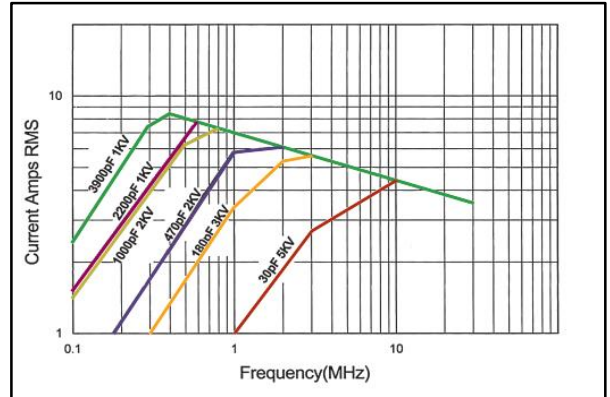
ESR VS Capacitance



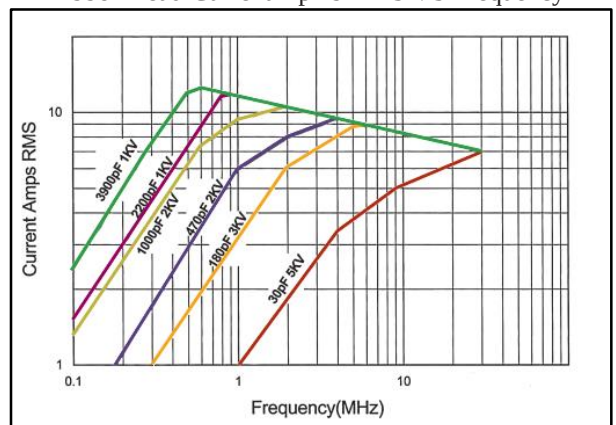
Current (max) VS Temperature



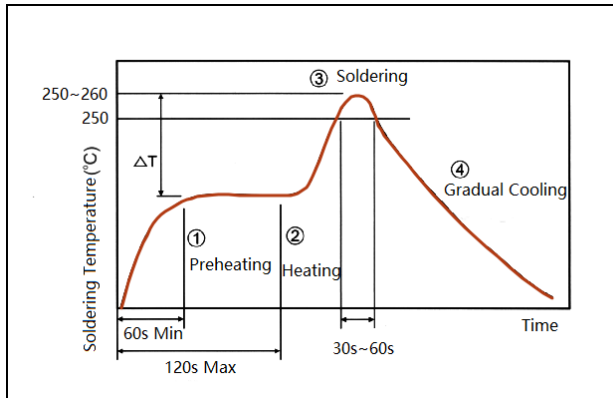
Wire Lead Current Apms RMS VS Frequency



Ribbon Lead Current Apms RMS VS Frequency



◆ Recommended soldering conditions



$\Delta T \leq 150^{\circ}\text{C}$;

Maximum temperature: $255^{\circ}\text{C} \pm 5^{\circ}\text{C}$;

Heating rate: $\leq 4^{\circ}\text{C/s}$.

◆ Recommended Land Dimensions

Horizontal Mounting

Orientation	EIA	A(mm)	B(mm)	C(mm)
Horizontal	6040	16.00	3.3	19.60

